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PATENT ABSTRACTS OF JAPAN

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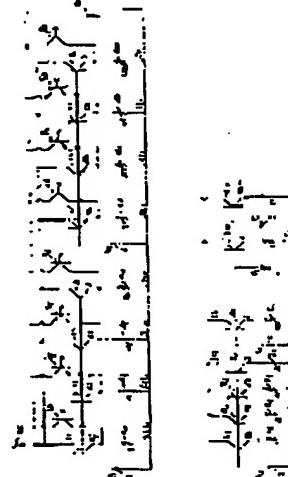
(21)Application number : 63-035557 (71)Applicant : SONY CORP
 (22)Date of filing : 18.02.1988 (72)Inventor : SHOJI NORIO

(54) D/A CONVERTER AND VOLTAGE GENERATING CIRCUIT

(57)Abstract:

PURPOSE: To reduce the maximum ratio of an emitter area or the like of current source transistor(TRs) by grouping the current source TRs and differentiating a base voltage to be applied or a power voltage at the emitter side from the group.

CONSTITUTION: TRs 1a, 1b, 1c... 1h constituting a constant current source are grouped at every 4-TR of high-order and low-order bit sides respectively, bases of the TRs 1e□1h at the high-order bit are connected to the voltage source 2 of a prescribed voltage VR in common and the bases of the TRs 1a□1d at the low-order bit are connected to the voltage source 3 of a voltage VX in common. Furthermore, the emitters of TRs Q1□Q5 for the current source are connected to other terminals of the voltage sources 2, 3 of voltages VR, VX through resistors Q1□Q5 with a resistance(r) inversely proportional to each weighting. Thus, the base voltage is made different to vary optionally the amplitude of the current flowing through the TRs, thereby reducing the maximum ratio of the emitter area of the TRs.



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